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CONFIRMATION NO. 7082

SERIAL NUMBER 10/829,380	FILING OR 371(c) DATE 04/22/2004 RULE	CLASS 327	GROUP ART UNIT 2816	ATTORNEY DOCKET NO. H-1139
APPLICANTS Atsuhiro Hayashi, Akisima, JAPAN; Takemi Negishi, Honjo, JAPAN; Hiroshi Toyoshima, Akiruno, JAPAN;				
** CONTINUING DATA ***** <i>none</i>				
** FOREIGN APPLICATIONS ***** JAPAN 2003-118528 04/23/2003				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/28/2004				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged Examiner's Signature _____ Initials _____		STATE OR COUNTRY JAPAN	SHEETS DRAWING 39	TOTAL CLAIMS 27 INDEPENDENT CLAIMS 3
ADDRESS 24956				
TITLE Semiconductor integrated circuit controlling output impedance and slew rate				
FILING FEE RECEIVED 1026	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	